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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Christophe J. Chevallier

Title:

SUPPLY VOLTAGE REDUCTION CIRCUIT FOR INTEGRATED CIRCUIT

Docket No.:

703.019US3

Serial No.: 09/955,270

Filed:

September 18, 2001

Due Date: January 16, 2003

Examiner:

Edward Wojciechowicz

Group Art Unit: 2815

Commissioner for Patents Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

A return postcard.

 $\frac{X}{X}$ An Amendment and Response (7 Pages).

Clean Version of Pending Claims (13 pgs.).

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 16th day of January, 2003.

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